receiving an incoming signal; and

comparing the oscillating reference against the incoming signal to detect a transition in the incoming signal relative to the known previous logical state.

6. (Once Amended) The method of claim 1, wherein the oscillating reference is received substantially synchronously with the incoming signal.

15. (Once Amended) The method of claim 11, wherein the oscillating reference is received substantially synchronously with the incoming signal.

23. (Twice Amended) A receiver comprising:

a first comparator for comparing an oscillating reference and a new signal;

a second comparator for comparing a complement of the oscillating reference and the new signal;

an output terminal coupled to one of the first and second comparators;

circuitry for maintaining the comparator that is coupled to the output terminal coupled to the output terminal when the new signal transitions; and

circuitry for coupling to the output terminal the comparator that is not coupled to the output terminal and de-coupling from the output terminal the comparator that is coupled to the output terminal when the new signal does not transition.

Please add *NEW* claims 28 through 33 as follows:

28. The system of claim 1, wherein the oscillating reference comprises a ramp signal.

29. The system of claim 1, wherein the oscillating reference is a discontinuously varying signal.

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30. The method of claim 11, wherein the oscillating reference comprises a ramp signal.

31. The method of claim 11, wherein the oscillating reference is a discontinuously varying signal.

32. The method of claim 11, wherein the first controller includes an exclusive-OR (XOR) logic gate.

33. The method of claim 19, wherein the second controller includes an exclusive-OR (XOR) logic gate.